

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	634	(detect\$4 with clock with stab\$7) and (lock\$4 with signal\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:26
L13	69	9 and (state with machine) and hardware	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:27
L14	74	9 and (state with machine) and (edge with detect\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:27
L15	55	9 and (state with machine) and hardware and (edge with detect\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:28

## EAST Search History

L16	13857	((703/13-22) or (716/1-21)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/10/30 15:28
L17	410	L16 AND (Clock with detect\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:28
L18	172	L17 and (control\$4 adj2 signal) and (signal with generat\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:28
L19	67	L18 and (state with machine)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:28

## EAST Search History

L20	7561	((clock with (stabilization or stabl\$4)) or (signal with lock\$4)) and (state with machine)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:33
L21	2499	(clock with detect\$4) and L20	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:33
L22	90	L21 and (digital with clock with module) and (select\$4 with (circuit or multiplex\$3))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:33
L23	55	L22 and (buffer same feedback same signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:33

## EAST Search History

L24	55	L23 and (input\$4 with signal) and (output\$4 with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:33
L25	54	L24 and (programmable with logic\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:33
L26	54	L25 and (state with machine same register\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 15:35
L29	9	9 and (mask\$4 with output with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/30 16:05